and the compiler phase in which the node was merged is marked. Finally, the action is marked as MERGED 1335.

Figure 14 is a flowchart illustrating annotation branch inversion according to one embodiment of the present invention. First, a new action node is created 1405. The major

ID from the action node of the branch being inverted is copied to the new action node 1310. A new action number is assigned to the new node 1315. Next, the previous actions pointer of the new node is set to the action node of the branch being inverted and the compiler phase in which the node was merged is marked. Finally, the action is marked as BRANCH_INVERTED 1335.

10

5

CLAIMS

2 2

What is claimed is:

A method comprising:

- installing a program onto a target machine;
- 3 executing the program; and
- 4 responsive to a change in profile data collected while the program executes which
- 5 \exceeds a predetermined threshold, recompiling the program while the
- 6 target machine is idle.
- 1 2. The method of claim of claim 1, wherein said installing further comprises:
- 2 installing a continuous compiler;

Docket No: 042390.P8130

Express Mail No: EL591668285US

18

	_	
400 X6	7	•
3	1	installing a runtime monitor;
4		copying an intermediate representation of a source file to the target machine;
5		building a profile database; and
6		compling the intermediate representation to create an executable file.
1	3.	The method of claim 1, wherein said executing further comprises:
2		running an executable version of the program;
3		collecting samples of process information at a controlled rate; and
4		while the target machine is idle, generating binary level and high level profiles.
1	4.	The method of claim 1, wherein recompiling further comprises customizing
2		compiler optimizations based on profile data generated during program execution.
1	5.	The method of claim 4, wherein said customizing compiler optimizations is
2		performed using annotations in a high level representation of an executable
3		program which relate portions of the executable to the high level representation.
1	6.	The method of claim 6, wherein creating said annotations comprises:
2		creating a new action node;
3		assigning to the new action node a major ID from a precomputed ID;
4		assigning a new action number to the new action node;
5		setting a previous action node pointer of the new action node to NULL;
6		marking a compiler phase in which the new node was created; and
7		marking an action of the new node as created.
1	7.	The method of claim 6, wherein duplicating an annotation comprises:
2		creating two new action nodes;
	Daales	• No. 042200 D9120

Docket No: 042390.P8130 Express Mail No: EL591668285US

	_
Sub He T	copying a major ID to the new action nodes from an action node of instructions
4	being copied;
5	assigning new action numbers to the two new nodes;
6	setting previous action node pointers of the new nodes to the action node being
7	copied;
8	marking a compiler phase in which the nodes was duplicated; and
9	marking an action of the new nodes as duplicated.
1 8.	The method of claim 6, wherein deleting an annotation comprises:
2	creating a new action node;
3	copying a major ID from an action node of an instruction being deleted to the new
4	action node;
5	assigning a new action number to the new action node;
6	setting a previous action pointer in the new action node to the action node of the
7	instruction being deleted;
8	marking a compiler phase in which the node was deleted; and
9	marking an action of the deleted node as deleted.
1 9.	The method of claim , wherein merging of annotations comprises:
2	creating a new action node;
3	copying a major ID from a previous action node of instructions being merged;
4	assigning a new action number to the new action node;
5	setting a previous action pointer of the new node to a list of nodes of the
6	instruction being merged;
	ket No: 042390.P8130 \ress Mail No: EL591668285US 20

of instructions, the sequences of instructions which, when executed by a processor, cause the processor to perform the steps of: installing a program onto a target machine; executing the program; and			
marking a compiler phase in which the node was merged; marking an action of the new node as merged. The method of claim 6, wherein annotation branch inversion comprises: creating a new action node; copying a major ID from a branch instruction being inverted; assigning a new action number to the new node; setting a previous action pointer of the new node to a node of a branch being inverted; marking a compiler phase in which the inversion occurred; and marking the branch as inverted. A computer readable medium having stored thereon data representing sequences of instructions, the sequences of instructions which, when executed by a processor, cause the processor to perform the steps of: installing a program onto a target machine; executing the program, and responsive to a change in profile data collected while the program executes which exceeds a predetermined threshold, recompiling the program while the target machine is idle. The computer readable medium of claim of claim 11, wherein said installing further comprises: installing a continuous compiler;	4-41	*7	adding the new action to a next actions pointer list of previous actions:
marking an action of the new node as merged. The method of claim 6, wherein annotation branch inversion comprises: creating a new action node; copying a major ID from a branch instruction being inverted; assigning a new action number to the new node; setting a previous action pointer of the new node to a node of a branch being inverted; marking a compiler phase in which the inversion occurred; and marking the branch as inverted. A computer readable medium having stored thereon data representing sequences of instructions, the sequences of instructions which, when executed by a processor, cause the processor to perform the steps of: installing a program onto a target machine; executing the program; and responsive to a change improfile data collected while the program executes which exceeds a predetermined threshold, recompiling the program while the target machine is idle. The computer readable medium of claim of claim 11, wherein said installing further comprises: installing a continuous compiler;	٠ 8	·	
1 10. The method of claim 6, wherein annotation branch inversion comprises: 2 creating a new action node; 3 copying a major ID from a branch instruction being inverted; 4 assigning a new action number to the new node; 5 setting a previous action pointer of the new node to a node of a branch being 6 inverted; 7 marking a compiler phase in which the inversion occurred; and 8 marking the branch as inverted. 1 11. A computer readable medium having stored thereon data representing sequences 2 of instructions, the sequences of instructions which, when executed by a 3 processor, cause the processor to perform the steps of: 4 installing a program onto a target machine; 5 executing the program; and 6 responsive to a change in profile data collected while the program executes which 7 exceeds a predetermined threshold, recompiling the program while the 8 target machine is idle. 1 12. The computer readable medium of claim of claim 11, wherein said installing 9 further comprises: 1 installing a continuous compiler;	0		making a complici phase in which the node was merged,
creating a new action node; copying a major ID from a branch instruction being inverted; assigning a new action number to the new node; setting a previous action pointer of the new node to a node of a branch being inverted; marking a compiler phase in which the inversion occurred; and marking the branch as inverted. 11. A computer readable medium having stored thereon data representing sequences of instructions, the sequences of instructions which, when executed by a processor, cause the processor to perform the steps of: installing a program onto a target machine; executing the program; and responsive to a change in profile data collected while the program executes which exceeds a predetermined threshold, recompiling the program while the target machine is idle. 12. The computer readable medium of claim of claim 11, wherein said installing further comprises: installing a continuous compiler;	9		marking an action of the new node as merged.
copying a major ID from a branch instruction being inverted; assigning a new action number to the new node; setting a previous action pointer of the new node to a node of a branch being inverted; marking a compiler phase in which the inversion occurred; and marking the branch as inverted. 1 11. A computer readable medium having stored thereon data representing sequences of instructions, the sequences of instructions which, when executed by a processor, cause the processor to perform the steps of: installing a program onto a target machine; executing the program; and responsive to a change in profile data collected while the program executes whice exceeds a predetermined threshold, recompiling the program while the target machine is idle. 1 12. The computer readable medium of claim of claim 11, wherein said installing further comprises: installing a continuous compiler;	1	10.	The method of claim 6, wherein annotation branch inversion comprises:
assigning a new action number to the new node; setting a previous action pointer of the new node to a node of a branch being inverted; marking a compiler phase in which the inversion occurred; and marking the branch as inverted. 1 11. A computer readable medium having stored thereon data representing sequences of instructions, the sequences of instructions which, when executed by a processor, cause the processor to perform the steps of: installing a program onto a target machine; executing the program; and responsive to a change in profile data collected while the program executes which exceeds a predetermined threshold, recompiling the program while the target machine is idle. 1 12. The computer readable medium of claim of claim 11, wherein said installing further comprises: installing a continuous compiler;	2		creating a new action node;
setting a previous action pointer of the new node to a node of a branch being inverted; marking a compiler phase in which the inversion occurred; and marking the branch as inverted. 1 11. A computer readable medium having stored thereon data representing sequences of instructions, the sequences of instructions which, when executed by a processor, cause the processor to perform the steps of: installing a program onto a target machine; executing the program; and responsive to a change in profile data collected while the program executes which exceeds a predetermined threshold, recompiling the program while the target machine is idle. 1 12. The computer readable medium of claim of claim 11, wherein said installing further comprises: installing a continuous compiler;	3		copying a major ID from a branch instruction being inverted;
marking a compiler phase in which the inversion occurred; and marking the branch as inverted. 1	4		assigning a new action number to the new node;
marking a compiler phase in which the inversion occurred; and marking the branch as inverted. 1 11. A computer readable medium having stored thereon data representing sequences of instructions, the sequences of instructions which, when executed by a processor, cause the processor to perform the steps of: installing a program onto a target machine; executing the program; and responsive to a change improfile data collected while the program executes which exceeds a predetermined threshold, recompiling the program while the target machine is idle. 1 12. The computer readable medium of claim of claim 11, wherein said installing further comprises: installing a continuous compiler;	5		setting a previous action pointer of the new node to a node of a branch being
marking the branch as inverted. 1 11. A computer readable medium having stored thereon data representing sequences 2 of instructions, the sequences of instructions which, when executed by a 3 processor, cause the processor to perform the steps of: 4 installing a program onto a target machine; 5 executing the program; and 6 responsive to a change in profile data collected while the program executes which 7 exceeds a predetermined threshold, recompiling the program while the 8 target machine is idle. 1 12. The computer readable medium of claim of claim 11, wherein said installing 2 further comprises: 3 installing a continuous compiler;	6		inverted;
1 11. A computer readable medium having stored thereon data representing sequences 2 of instructions, the sequences of instructions which, when executed by a 3 processor, cause the processor to perform the steps of: 4 installing a program onto a target machine; 5 executing the program; and 6 responsive to a change in profile data collected while the program executes which 7 exceeds a predetermined threshold, recompiling the program while the 8 target machine is idle. 1 12. The computer readable medium of claim of claim 11, wherein said installing 2 further comprises: 3 installing a continuous compiler;	7		marking a compiler phase in which the inversion occurred; and
of instructions, the sequences of instructions which, when executed by a processor, cause the processor to perform the steps of: installing a program onto a target machine; executing the program; and responsive to a change in profile data collected while the program executes which exceeds a predetermined threshold, recompiling the program while the target machine is idle. The computer readable medium of claim of claim 11, wherein said installing further comprises: installing a continuous compiler;	8		marking the branch as inverted.
processor, cause the processor to perform the steps of: installing a program onto a target machine; executing the program; and responsive to a change in profile data collected while the program executes whice exceeds a predetermined threshold, recompiling the program while the target machine is idle. The computer readable medium of claim of claim 11, wherein said installing further comprises: installing a continuous compiler;	1	11.	A computer readable medium having stored thereon data representing sequences
installing a program onto a target machine; executing the program; and responsive to a change in profile data collected while the program executes whice exceeds a predetermined threshold, recompiling the program while the target machine is idle. The computer readable medium of claim of claim 11, wherein said installing further comprises: installing a continuous compiler;	2		of instructions, the sequences of instructions which, when executed by a
executing the program; and responsive to a change in profile data collected while the program executes whice exceeds a predetermined threshold, recompiling the program while the target machine is idle. The computer readable medium of claim of claim 11, wherein said installing further comprises: installing a continuous compiler;	3		processor, cause the processor to perform the steps of:
responsive to a change in profile data collected while the program executes whice exceeds a predetermined threshold, recompiling the program while the target machine is idle. The computer readable medium of claim of claim 11, wherein said installing further comprises: installing a continuous compiler;	4		installing a program onto a target machine;
exceeds a predetermined threshold, recompiling the program while the target machine is idle. The computer readable medium of claim 11, wherein said installing further comprises: installing a continuous compiler;	5		executing the program; and
target machine is idle. The computer readable medium of claim 11, wherein said installing further comprises: installing a continuous compiler;	6		responsive to a change in profile data collected while the program executes which
1 12. The computer readable medium of claim of claim 11, wherein said installing 2 further comprises: 3 installing a continuous compiler;	7		exceeds a predetermined threshold, recompiling the program while the
2 further comprises: 3 installing a continuous compiler;	8		target machine is idle.
3 installing a continuous compiler;	1	12.	The computer readable medium of claim of claim 11, wherein said installing
	2		further comprises:
Docket No: 042390.P8130	3		installing a continuous compiler;
Express Mail No: EL591668285US 21			

Sub	?	installing a runtime monitor;
4		instaining a function in the monitor,
5		copying an intermediate representation of a source file to the target machine;
6		building a profile database; and
7		compiling the intermediate representation to create an executable file.
1	13.	The computer readable medium of claim 11, wherein said executing further
2		comprises:
3		running an executable version of the program;
4		collecting samples of process information at a controlled rate; and
5		while the target machine is idle, generating binary level and high level profiles.
1	14.	The computer readable medium of claim 11, wherein recompiling further
2		comprises customizing compiler optimizations based on profile data generated
3		during program execution.
1	15.	The computer readable medium of claim 14, wherein said customizing compiler
2		optimizations is performed using annotations in a high level representation of an
3		executable program which relate portions of the executable to the high level
4		representation.
1	16.	The computer readable medium of claim 16, wherein creating said annotations
2		comprises:
3		creating a new action node;
4		assigning to the new action node a major ID from a precomputed ID;
5		assigning a new action number to the new action node;
6		setting a previous action node pointer of the new action node to NULL;
	Docke	et No: 042390.P8130

Express Mail No: EL591668285US

(30)	×7	
7	`	marking a compiler phase in which the new node was created; and
8		marking an action of the new node as created.
1	17.	The computer readable medium of claim 16, wherein duplicating an annotation
2		comprises:
3		creating two new action nodes;
4		copying a major ID to the new action nodes from an action node of instructions
5		being copied;
6		assigning new action numbers to the two new nodes;
7		setting previous action node pointers of the new nodes to the action node being
8		copied:
9		marking a compiler phase in which the nodes was duplicated; and
10		marking an action of the new nodes as duplicated.
1	18.	The computer readable medium of claim 16, wherein deleting an annotation
2		comprises:
3		creating a new action node;
4		copying a major ID from an action node of an instruction being deleted to the new
5		action node;
6		assigning a new action number to the new action node;
7		setting a previous action pointer in the new action node to the action node of the
8		instruction being deleted;
9		marking a compiler phase in which the node was deleted; and
10		marking an action of the deleted node as deleted.
		t No: 042390.P8130 ss Mail No: EL591668285US 23

Sub C	10.	The computer readable medium of claim 16, wherein merging of annotations
2	1	comprises:
3		creating a new action node;
4		copying a major ID from a previous action node of instructions being merged;
5		assigning a new action number to the new action node;
6		setting a previous action pointer of the new node to a list of nodes of the
7		instruction being merged;
8		adding the new action to a next actions pointer list of previous actions;
9		marking a compiler phase in which the node was merged;
10		marking an action of the new node as merged.
1	20.	The computer readable medium of claim 16, wherein annotation branch inversion
2		comprises:
3		creating a new action node;
4		copying a major Ip from a branch instruction being inverted;
5		assigning a new action number to the new node;
6		setting a previous action pointer of the new node to a node of a branch being
7		inverted;
8		marking a compiler phase in which the inversion occurred; and
9		marking the branch as inverted.
1	21.	A system comprising:
2		a storage device having stored therein a routine for transparent continuous
3		compilation;
		t No: 042390.P8130 ss Mail No: EL591668285US 24

ı

Sul	Hb /	7
4	` /	a processor coupled to the storage device which when executing the routine:
5		installs a program onto a target machine;
6		executes the program; and
7		responsive to a change in profile data collected while the program executes which
8		exceeds a predetermined threshold, recompiles the program while the
9		target machine is idle.
1	22.	The system of claim of claim 21, wherein said installation further comprises:
2		installing a continuous compiler;
3		installing a runtime monitor;
4		copying an intermediate representation of a source file to the target machine;
5		building a profile database; and
6		compiling the intermediate representation to create an executable file.
1	23.	The system of claim 21, wherein said execution further comprises:
2		running an executable version of the program;
3		collecting samples of process information at a controlled rate; and
4		while the target machine is idle, generating binary level and high level profiles.
1	24.	The system of claim 21, wherein recompilation further comprises customizing
2		compiler optimizations based on profile data generated during program execution.
1	25.	The system of claim 24, wherein said customizing compiler optimizations is
2		performed using annotations in a high level representation of an executable
3		program which relate portions of the executable to the high level representation.
1	26.	The system of claim 26, wherein creating said annotations comprises:
		t No: 042390.P8130 ss Mail No: EL591668285US 25

1 ...

	_
500 A67	creating a new action node;
3	assigning to the new action node a major ID from a precomputed ID;
4	assigning a new action number to the new action node;
5	setting a previous action node pointer of the new action node to NULL;
6	marking a compiler phase in which the new node was created; and
7	marking an action of the new node as created.
1 27.	The system of claim 26, wherein duplicating an annotation comprises:
2	creating two new action nodes;
3	copying a major ID to the new action nodes from an action node of instructions
4	being copied;
5	assigning new action numbers to the two new nodes;
6	setting previous action node pointers of the new nodes to the action node being
7	copied;
8	marking a compiler phase in which the nodes was duplicated; and
9	marking an action of the new nodes as duplicated.
1 28.	The system of claim 26, wherein deleting an annotation comprises:
2	creating a new action note;
3	copying a major ID from an action node of an instruction being deleted to the new
4	action node;
5	assigning a new action number to the new action node;
6	setting a previous action pointer in the new action node to the action node of the
7	instruction being deleted;
	tet No: 042390.P8130 \ ess Mail No: EL591668285US 26

Express Mail No: EL591668285US

1

. . .

ران 8	A67	marking a compiler phase in which the node was deleted; and
9		marking an action of the deleted node as deleted.
1	29.	The system of claim 26, wherein merging of annotations comprises:
2		creating a new action node;
3		copying a major ID from a previous action node of instructions being merged
4		assigning a new action number to the new action node;
5		setting a previous action pointer of the new node to a list of nodes of the
6		instruction being merged;
7		adding the new action to a next actions pointer list of previous actions;
8		marking a compiler phase in which the node was merged;
9		marking an action of the new node as merged.
1	30.	The system of claim 26, wherein annotation branch inversion comprises:
2		creating a new action node;
3		copying a major ID from a branch instruction being inverted;
4		assigning a new action number to the new node;
5		setting a previous action pointer of the new node to a node of a branch being
6		inverted;
7		marking a compiler phase in which the inversion occurred; and
8		marking the branch as inverted.
	Add	(R77)

. 1

Docket No: 042390.P8130

Express Mail No: EL591668285US